LIU Junxiu

BASIC INFORMATION

Gender & Birth. Male & 1984-10-13
University Guangdong University of Technology
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OBJECT

INTERESTS

Embedded system, Software design

EDUCATION

Sept. 2007 **Faculty of Information Engineering, Guangdong University of Technology** Guangzhou, Guangdong
M.Sc. in Signal and Information Processing, degree expected in July 2010
- Overall GPA: 90.3/100, 3.61/4
- Rank: Top 5% among the 64 students

2003-2007 **Faculty of Physics and Electronic, Hunan Institute of Science and Technology** Yueyang, Hunan
B.E in Electronic Information
- Overall GPA: 81.2/100, 3.23/4
- Rank: Top 5% among the 353 students

TECHNICAL EXPERIENCE

Languages C, C++, UNIX shell scripts, HDL
Platforms Windows XP, Red hat Linux 9.0

2007-present **Embedded Image Processing System**

This system is a metallographic quantitative analysis system. PowerPC 405 inside of Xilinx FPGA is the main processor, and Linux 2.6 operating system, QT 2.3.2 and OpenCV are used.

Through the EDK dedicated by Xilinx, we can conveniently construct an embedded system using PowerPC405, and user custom IP core connecting with PLB, OPB. The embedded Linux system is used because of its excellent task scheduling mechanisms and the memory (DDR 256M) is extended according to the huge application. QT provides excellent GUI, and generic algorithms library is provided by OpenCV. The main processor is a little slowly, so now we try our best to add a user custom IP core to system, which can result in a dramatic acceleration in software execution time due to algorithms being executed in parallel in hardware and not sequentially in software.

Now, the basic hardware, software platform is constructed completely.

EXPERIENCE

2008-present **www.eefocus.com**
Campus Ambassador
- Spread electronic technologies, platforms and development tools provide by "eefocus" to professors, researchers, faculty, students by tech talk, salon, and training.

2007.2-2007.9 **Jinbenteng Auto S&T Co., Ltd.** Beijing
Development Engineer
JBT is providing the automobile diagnostic scanner, automobile equipments and service solution since 1999.
- In change of writing the Benz simulation MCU routine and the communication protocol between the diagnostic scanner and the automobile MCU;
- Platform is 8 monolithic integrated circuits, using assemble language.

2006-2007 **Yueyang Vocational Technical Education School** Yueyang, Hunan
Physic Teacher
- Teaching Grade 2 physic lessons and laboratory courses.

LANGUAGE PROFICIENCY

Chinese	Native
English	CET 6

HONOR & REWARDS

2008	"UP-TECH" Embedded System Design Contest third-class prize	
2007	Excellent graduate of Hunan Province	12 persons of 18,994
2006	Excellent Tri-A Student of Hunan Province	24 persons of 18,994
2005	Moving Persons of Hunan Institute of Science and Technology	8 persons of 18,994
2005	Financial Aid	36 persons of 18,994
2004	National scholarship	24 persons of 18,994
2004	Excellent Tri-A Student of Hunan Institute of Science and Technology	
2004	PAN Guanghui scholarship	
2003	Major award of Hunan Institute of Science and Technology	

PAPER

- ***LIU Junxiu***, WU Liming (Supervisor), DENG Yaohua. "A Design of Embedded System in Data Acquisition Based on FPGA," Chinese Journal of Scientific Instrument, vol.30, No. 6, pp. 316-318, 2009(6): in Chinese
- WU Liming (Supervisor), ***LIU Junxiu***, DAI Min. "Single Chip Fuzzy Control System Based on Mixed-Signal FPGA", 2009 International Conference on Intelligent Human-Machine Systems and Cybernetics, China Hangzhou. [EI]
- WU Liming (Supervisor), ***LIU Junxiu***, ZHANG Ji, LUO Yuling. "The Intelligent Reconfigurable Measuring Node Based on Wireless Access Network", 2009 International Conference on Computational Intelligence and Security, China Beijing. [EI]
- WU Liming (Supervisor), ***LIU Junxiu***, LUO Yuling. "The Implementation and Evaluation of Hardware/Software Co-design Method for Fast Image Processing based on FPGA", 2009International Conference on Computer Science and Software Engineering, China Wuhan. [EI]
- WU Liming (Supervisor), ***LIU Junxiu***, LUO Yuling. "The Design of Co-processor for the Image Processing Single Chip System", 2009 International Conference on Computer Sciences and Convergence Information Technology, Korea Seoul. [EI]

REFEREES

Available upon request